

PAT-NO: JP356074749A

DOCUMENT-IDENTIFIER: JP 56074749 A

TITLE: MICROPROGRAM  
CONTROLLING DEVICE

----- KWIC -----

Abstract Text - FPAR (2):

CONSTITUTION: As for the microprogram, the micro-instruction is read out to the micro-instruction register 3 from the control memory by the (n) bit of the address stored in the micro-instruction address register 2. Also at the same time, the decode mode is read out to the decode mode register 6 from the decode mode memory by use of the high rank (a) bit of the address stored in the register 2. The decoder 4 decodes the micro-instruction stored in the micro-instruction register 3, and modifies the decode output prescribed by the decode mode which has been stored in the register 6.

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PUBN-DATE: June 20, 1981

INVENTOR-INFORMATION:

NAME

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ASSIGNEE-INFORMATION:

NAME

COUNTRY

NEC CORP

N/A

APPL-NO: JP54152664

APPL-DATE: November 26, 1979

INT-CL (IPC): G06F009/22

ABSTRACT:

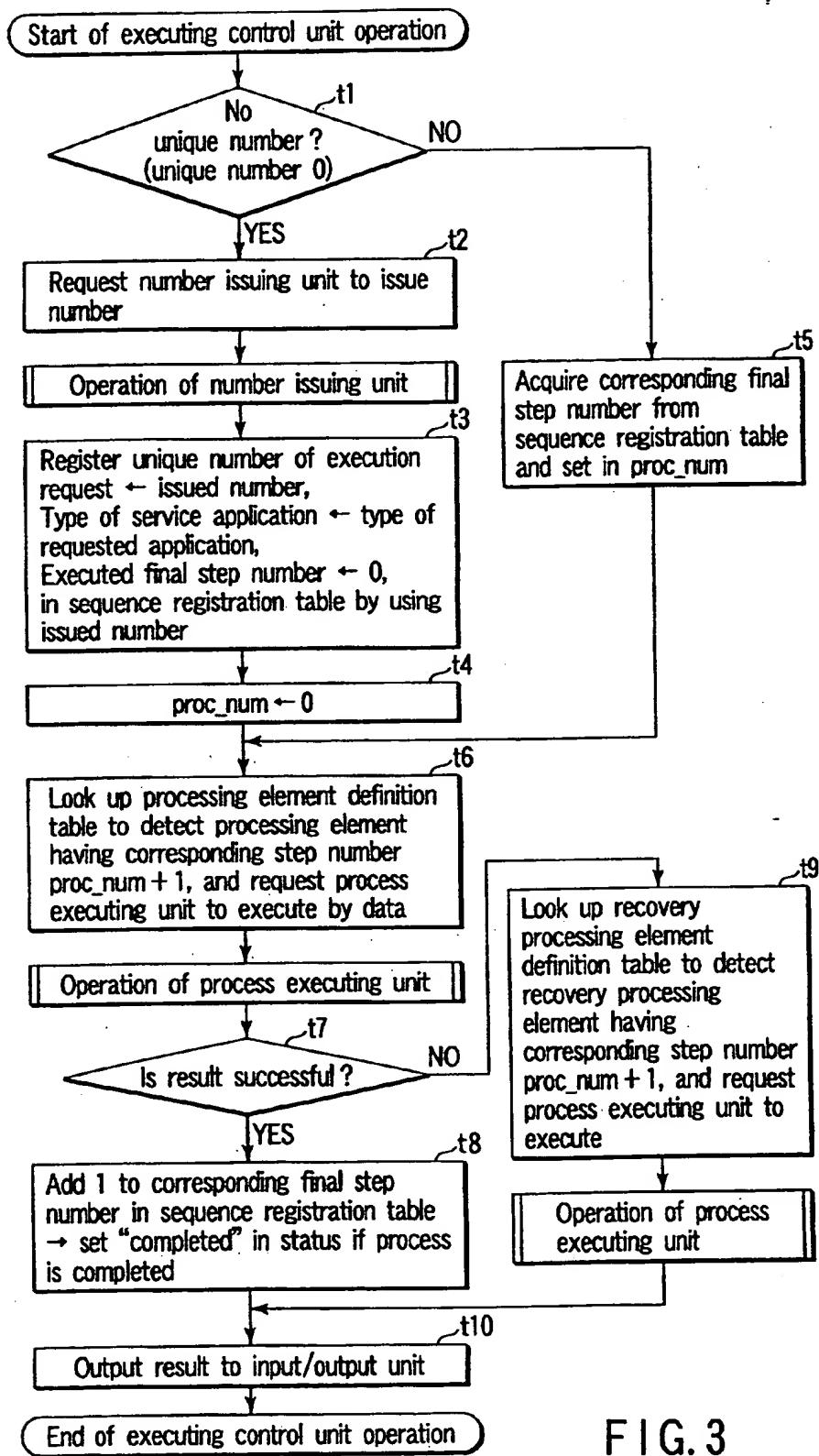


FIG. 3

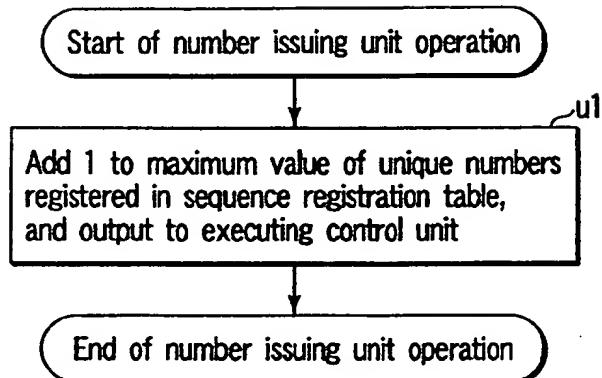


FIG. 4

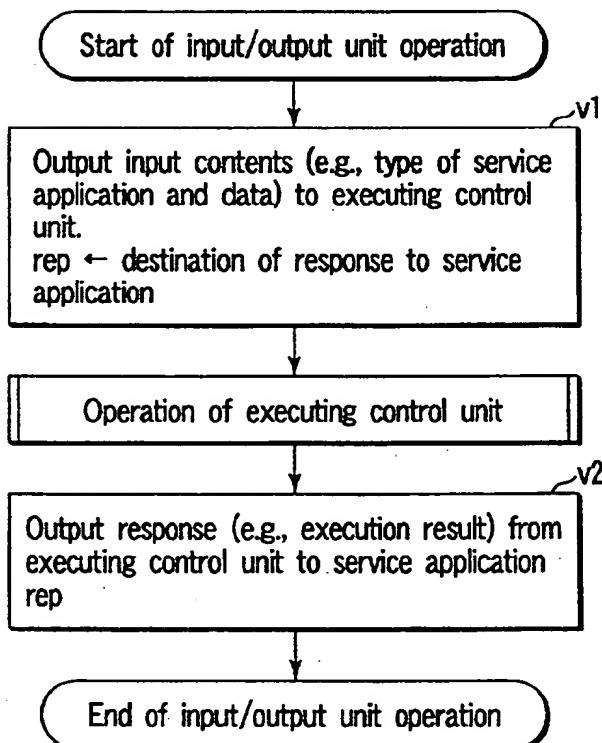


FIG. 5